

Attorney Ref No. 112.P91016

Patent Appl. No. 10/726,640

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AUG 02 2007AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior version, and listings, of claims in the application. Where claims have been amended and/or canceled, such amendments and/or cancellations are done without prejudice and/or waiver and/or disclaimer to the claimed and/or disclosed subject matter, and the applicant and/or assignee reserves the right to claim this subject matter and/or other disclosed subject matter in a continuing application.

Listing of Claims:

1 - 3. (Canceled)

4. (Currently amended) An apparatus comprising:  
an interface device including a board having:

a plurality of first contact receptacles members of a first depth length, at least one of the first contact receptacles members including a first end connected to the board and a second end to connect to an external device; and

at least two second contact receptacles members of a second depth length, wherein the at least two second contact receptacles members include one contact receptacle member connected to a first voltage line of a first VSS-voltage level, and another contact receptacle member connected to a second voltage line of a second VDD-voltage level or a VCC voltage level;

wherein the second depth length is greater than the first depth length such that[[[,] the second contact receptacles members are adapted to contact the external device prior to the first contact receptacles members contacting said the external device in response to the board being coupled to the external device through the interface device in a direction.

5 - 27. (Canceled)

28. (Previously Presented) A system, comprising:

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a plurality of first pins formed on a first board;  
a second board including a first surface and a second surface;  
a plurality of first contact points formed on the first surface of the second board to receive the first pins; and

a plurality of second pins formed on the second surface of the second board, the plurality of second pins adapted to be coupled to integrated circuit contact points,

wherein the plurality of first pins includes one or more first pins that are longer in length than the other first pins, or the plurality of second pins includes one or more second pins that are longer in length than the other second pins.

29 – 30. (Canceled)

31. (Currently amended) A method, comprising:

providing an interface device formed on a board, the interface device including:

a plurality of first contact receptacles members of a first depth length, at least one of the first contact receptacles members including a first end connected to the board and a second end to connect to an external device; and

at least two second contact receptacles members of a second depth length, wherein the at least two second contact receptacles members include one connected to a first voltage line of a first VSS-voltage level, and another connected to a second voltage line of a second VDD-voltage level or a VCC-voltage level; and

providing the at least two second contact receptacles members with a depth length greater than the depth length of at least one of the first contact receptacles to discharge electric charges accumulated on the board via the at least two second contact receptacles if the board is coupled to the external device through the interface device members;

~~coupling the board to the external device through the interface device; and~~  
~~discharging electric charges accumulated on the board via the at least two second contact members.~~

32 – 43. (Canceled)

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44. (Currently amended) A method, comprising:

providing a test device including a first board, the first board including a plurality of first pins;

providing a second board including a first surface and a second surface, the second board including a plurality of first contact points on the first surface to receive the first pins, and a plurality of second pins on the second surface, wherein at least one of the first pins is longer than the other first pins, or at least one of the second pins is longer than that of the other second pins; and

providing a plurality of second contact points on a plurality of integrated circuits to receive the second pins to discharge electric charges accumulated on a third board on which the integrated circuits are formed if the first pins are coupled to the first contact points and the second pins to the second contact points;

~~coupling the first pins to the first contact points and the second pins to the second contact points; and~~

~~discharging electric charges accumulated on a third board on which the integrated circuits are formed.~~

45 – 47. (Canceled)

48. (Currently amended) A system, comprising:

a plurality of first pins formed on a first board;

a second board including a first surface and a second surface;

a plurality of contact receptacles formed on the first surface of the second board to receive the first pins; and

a plurality of second pins formed on the second surface of the second board,

wherein the plurality of contact receptacles includes one or more contact receptacles that are deeper than the other contact receptacles, or the plurality of second pins includes one or more second pins that are longer in length than the other second pins.

~~An apparatus, comprising:~~

~~a board including:~~

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~~a plurality of first contact lines having ends extending toward but not reaching an edge of the board;~~

~~two second contact lines having ends extending to the edge of the board; and~~

~~a third contact line formed adjacent a periphery of the board and having first and second ends extending to the edge of the board.~~

49. (Currently amended) The apparatus of claim 48, wherein the plurality of second pins are adapted to be coupled to integrated circuit contact points ~~one second contact line is adapted to be connected to a first voltage level and the other second contact line is adapted to be connected to a second voltage level.~~

50. (Currently amended) The apparatus of claim 48 ~~[[49]]~~, wherein all of the first pins have equal lengths ~~voltage level comprises a VDD voltage level and the second voltage level comprises a VSS voltage level.~~

51-53. (Cancelled)

54. (Currently amended) A detecting system, comprising:  
a plurality of first pins formed on a test head;  
an interconnect board including a first surface and a second surface;  
a plurality of contact receptacles formed on the first surface of the interconnect board to receive the first pins; and  
a plurality of second pins formed on the second surface of the interconnect board,  
wherein the plurality of contact receptacles includes one or more contact receptacles that are deeper than the other contact receptacles, so that the one or more contact receptacles are adapted to contact the first pins prior to the other contact receptacles contacting the first pins in response to the test head being coupled to the interconnect board.

~~An apparatus, comprising:~~

~~a board including:~~

~~a plurality of first contact lines having ends disposed at a first distance from an edge of the board; and~~

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~~at least one second contact line disposed between two lines of the plurality of first contact lines, the at least one second line having an end disposed at a second distance from the edge of the board,~~  
~~wherein the second distance is shorter than the first distance.~~

55-59. (Cancelled)

60. (Currently amended) A method, comprising:

arranging a test head having a plurality of first pins adjacent to an interconnect board that includes a first surface and a second surface, the first surface including a plurality of contact receptacles, and the second surface including a plurality of second pins; and  
coupling the test head to the interconnect board so that the contact receptacles receive the first pins,

wherein the plurality of contact receptacles includes one or more contact receptacles that are deeper than the other contact receptacles, so that the one or more contact receptacles are adapted to contact the first pins prior to the other contact receptacles contacting the first pins in response to the test head being coupled to the interconnect board.

coupling a board to a device; wherein the board comprises:  
\_\_\_\_\_ a plurality of first contact lines each having an end disposed at a first distance from an edge of the board; and  
\_\_\_\_\_ at least one second contact line disposed between two lines of the plurality of first contact lines, the at least one second line having an end disposed at a second distance from the edge of the board,  
\_\_\_\_\_ wherein the second distance is shorter than the first distance; and  
\_\_\_\_\_ discharging electric charges accumulated on the board via the at least one second contact line.

61. (Currently amended) The method of claim 60, further comprising connecting the second pins to a board that includes integrated circuits wherein the end of the at least one second contact line extends to the edge of the board.

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62-64. (Cancelled)

65. (Currently amended) An apparatus, comprising:

a board for an integrated circuit tester, the board including a surface having a plurality of contact pins of a first length and at least one contact pin of a second length longer than the first length;

a second board including a first surface and a second surface;

a plurality of first contact points formed on the first surface of the second board to receive the contact pins; and

a plurality of second pins formed on the second surface of the second board, the plurality of second pins adapted to be coupled to integrated circuit contact points.

66. (Previously Presented) The apparatus of claim 65, wherein the board comprises one of an interface board or an interconnect board.

67. (Previously Presented) The apparatus of claim 65, wherein the plurality of contact pins of a first length and the at least one contact pin of a second length comprise pogo pins.

68. (Currently amended) The apparatus of claim 65, wherein the plurality of contact pins of a first length are adapted to contact input/output terminals of an integrated circuit mounted on the [[a]] second board, and wherein the at least one contact pin of the [[a]] second length is adapted to discharge current from the second board.

69. (Currently amended) The apparatus of claim 65, wherein the plurality of contact pins of the [[a]] first length are adapted to contact contact points of the [[a]] second board, and wherein the at least one contact pin of the [[a]] second length is adapted to discharge current from the second board.

70. (New) The apparatus of claim 4, wherein the first voltage level is VSS and the second voltage level is VDD or VCC.

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71. (New) The apparatus of claim 31, wherein the first voltage level is VSS and the second voltage level is VDD or VCC.